

Amendments to the Claims:

This listing of claims replaces all prior versions, and listings, of claims in the application:

Listing of Claims:

1-23. (Cancelled)

24. (Previously Presented) A method of driving a composite power amplifier having a first power amplifier entity and a first power amplifier entity pair, in turn including a second and a third power amplifier entity, the first, second and third power amplifier entities being connected to a common signal input over a control network and to a common load output over a combiner network, the method comprising the steps of:

determining a momentary amplitude of an input signal of the signal input and correlating the momentary input signal amplitude to a momentary output voltage amplitude; and

driving the first power amplifier entity with the input signal; and,

restricting the second and third power amplifier entities at momentary output voltage amplitudes below a first output voltage amplitude to deliver less current than a linearly increasing current function defined by zero current at zero momentary output voltage amplitude and a minimum required current at a momentary output voltage amplitude equal to a maximum linear output voltage that can be achieved with the first, second and third amplifier entities when the second and third amplifier entities deliver current with substantially 180 degrees phase difference;

wherein the first output voltage amplitude is larger than zero but less or equal to the maximum linear output voltage that can be achieved with the first, second and third amplifier entities.

25. (Previously Presented) The method according to claim 24, wherein the first output voltage amplitude is substantially equal to the maximum linear output voltage.

26. (Previously Presented) The method according to claim 24, wherein the restricting step comprises the step of prohibiting the second and third power amplifier entities from delivering any substantial current at momentary output voltage amplitudes below a second output voltage amplitude smaller than the first output voltage amplitude.

27. (Cancelled).

28. (Previously Presented) The method according claim 24, further comprising the steps of:

driving the first power amplifier entity at substantially maximum output voltage at momentary output voltage amplitudes above the second output voltage amplitude; and,

driving the second and third amplifier entities to deliver current with substantially 180 degrees phase difference at momentary output voltage amplitudes between the second output voltage amplitude and the first output voltage amplitude on the common load output.

29. (Previously Presented) The method according to claim 28, wherein the combiner network in turn comprises a first, a second and a third impedance conversion circuit, respectively, whereby the second and the third impedance conversion circuit have compensation elements having substantially the same absolute value but of opposite sign.

30. (Previously Presented) The method according to claim 28, further comprising the step of:

driving the second and third amplifier entities in an outphasing mode at momentary output voltage amplitudes above the first output voltage amplitude.

31. (Previously Presented) The method according to claim 24, wherein the composite power amplifier has at least one further power amplifier entity pair, whereby the method comprises the steps of taking each one of the at least one power amplifier

entity pair in use at respective third output voltage amplitudes and driving the at least one further power amplifier entity pair so that they deliver no current below the respective third output voltage amplitude.

32. (Previously Presented) The method according to claim 31, further comprising the step of driving the at least one further power amplifier entity pair to deliver current with substantially 180 degrees phase difference at momentary output voltage amplitudes between the respective third output voltage amplitude and a respective fourth output voltage amplitude on the common load output.

33. (Previously Presented) The method according to claim 32, further comprising the step of driving the at least one further power amplifier entity pair in an outphasing mode at momentary output voltage amplitudes above the respective fourth output voltage amplitude.

34. (Previously Presented) The method according to claim 24, wherein the first power amplifier entity in turn comprises power amplifiers in a Doherty arrangement, whereby the step of driving the first power amplifier entity at momentary output voltage amplitudes below the second output voltage amplitude, in turn comprises driving the Doherty arrangement with the input signal of the signal input.

35. (Previously Presented) Composite power amplifier, comprising:
a first power amplifier entity;
a first power amplifier entity pair, in turn including a second and a third power amplifier entity;
a common signal input;
a control network connecting the common signal input and the first, second and third power amplifier entities;
a common load output; and

a combiner network, connecting the first, second and third power amplifier entity, respectively, to the common load output over at least one impedance conversion circuit;

said control network in turn comprising means for determining a momentary amplitude of an input signal of the common signal input and relating the momentary input signal amplitude to a momentary output voltage amplitude;

wherein the control network is further arranged for restricting the second and third power amplifier entities at momentary output voltage amplitudes below a first output voltage amplitude to deliver less current than a linearly increasing current function defined by zero current at zero momentary output voltage amplitude and a minimum required current at a momentary output voltage amplitude equal to a maximum linear output voltage that can be achieved with the first, second and third amplifier entities when the second and third amplifier entities deliver current with substantially 180 degrees phase difference; and,

wherein the first output voltage amplitude is larger than zero but less or equal to the maximum linear output voltage that can be achieved with the first, second and third amplifier entities.

36. (Previously Presented) The composite power amplifier according to claim 35, wherein the first output voltage amplitude is substantially equal to the maximum linear output voltage.

37. (Previously Presented) The composite power amplifier according to claim 35, wherein the control network is arranged for prohibiting the second and third power amplifier entities from delivering any substantial current at momentary output voltage amplitudes below a second output voltage amplitude on the common load output smaller than the first output voltage amplitude.

38. (Cancelled).

39. (Previously Presented) The composite power amplifier according to claim 35, wherein said first power amplifier entity has a maximum output power P_0^{max} selected according to:

$$P_0^{max} < P_{tot}^{max} \cdot \frac{V_P}{V_{tot}^{max}},$$

where P_{tot}^{max} is a total maximum output power of all power amplifier entities, V_{tot}^{max} is a maximum output voltage amplitude and V_P is an output voltage amplitude corresponding to an intended efficiency maximum.

40. (Previously Presented) The composite power amplifier according to claim 35, wherein the control network is further arranged for driving the first power amplifier entity at substantially maximum output voltage at momentary output voltage amplitudes above the second output voltage amplitude and for driving the second and third amplifier entities to deliver current with substantially 180 degrees phase difference at momentary output voltage amplitudes between the second output voltage amplitude and the first output voltage amplitude on the common load output.

41. (Previously Presented) The composite power amplifier according to claim 40, wherein said combiner network in turn comprises a first, a second and a third impedance conversion circuit.

42. (Previously Presented) The composite power amplifier according to claim 41, wherein the second and the third impedance conversion circuits comprise compensation elements of substantially the same absolute value but of opposite sign.

43. (Cancelled).

44. (Previously Presented) The composite power amplifier according to claim 40, wherein the control network is further arranged for driving the second and third

amplifier entities in an outphasing mode at intended momentary output voltage amplitude responses above the first output voltage amplitude.

45. (Previously Presented) The composite power amplifier according to claim 35, further comprising at least one further power amplifier entity pair.

46. (Previously Presented) The composite power amplifier according to claim 35, wherein the first power amplifier entity in turn comprises power amplifiers in a Doherty arrangement.

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